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DATE MAILED: 01/14/2005

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/686,974	10/16/2003	Rakesh Batish	K&S-120US	8097
23122	7590 01/14/2005		EXAMINER	
RATNERPI			COLEMAN, WILLIAM D	
P O BOX 980)			
VALLEY FO	RGE, PA 19482-0980)	ART UNIT	PAPER NUMBER
			2823	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
Office Action Communication	10/686,974	BATISH ET AL.				
Office Action Summary	Examiner	Art Unit				
	W. David Coleman	2823				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1)⊠ Responsive to communication(s) filed on <u>13 D</u>	<u>ecember 2004</u> .	•				
2a) This action is FINAL . 2b) ■ This action is non-final.						
3)☐ Since this application is in condition for allowar	nce except for formal matters, pro	secution as to the merits is				
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims						
4)⊠ Claim(s) <u>1-23</u> is/are pending in the application.						
4a) Of the above claim(s) <u>12-23</u> is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-3 and 7-11</u> is/are rejected.						
7)⊠ Claim(s) <u>4-6</u> is/are objected to.						
8) Claim(s) are subject to restriction and/o	r election requirement.					
Application Papers						
9)☐ The specification is objected to by the Examiner.						
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of:						
1. Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau (PCT Rule 17.2(a)).						
* See the attached detailed Office action for a list of the certified copies not received.						
·						
Attachment(s)						
1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) Paper No(s)/Mail Date						
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)	5) Notice of Informal P	Patent Application (PTO-152)				
Paper No(s)/Mail Date 10/03 Work	6)					

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DETAILED ACTION

Election/Restrictions

1. Applicant's election without traverse of Group I invention, claims 1-11 in the reply filed on December 13, 2004 is acknowledged.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

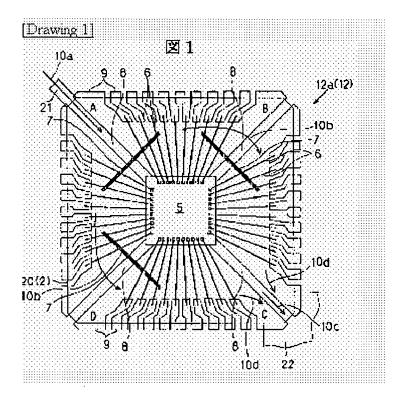
- (a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.
- 3. Claims 1, 2, 3, 7, 9, 10 and 11 are rejected under 35 U.S.C. 102(a) as being anticipated by Shimanuki et al., Japanese Patent Abstracts Publication No. 2002-368029.

 Shimanuki discloses a semiconductor process as claimed. See **Drawings 1-22**, where Shimanuki

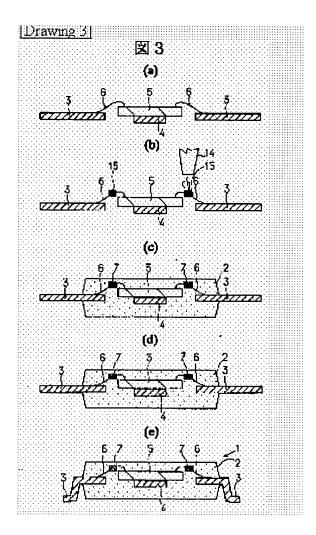
teaches the claimed limitations.

- 4. Pertaining to claim 1, <u>Shimanuki</u> teaches a method of packaging a semiconductor device, the method comprising the steps of:
- applying an insulative material 7 across only a portion of at least two of a plurality of conductors providing interconnection between elements in the semiconductor device; and encapsulating 2 the conductors and elements, thereby packaging the semiconductor device.

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5. Pertaining to claim 2, <u>Shimanuki</u> teaches the method of claim 1 further comprising the step of:

curing the insulative material after said applying step (see Drawing 3(b)).

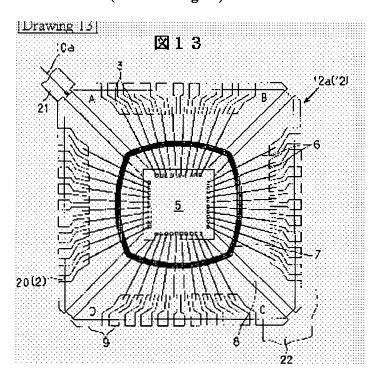
- 6. Pertaining to claim 3, Shimanuki teaches the method of claim 2 wherein said curing step includes at least one of heating the insulative material and exposing the insulative material to UV radiation (see paragraph [0018].
- 7. Pertaining to claim 7, <u>Shimanuki</u> teaches the method of claim 1 wherein said applying step includes applying a solid insulator having an adhesive backing to the

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portion of a plurality of conductors such that the adhesive backing is in contact with the portion of a plurality of conductors (see Drawing 18).

- 8. Pertaining to claim 8, Shimanuki teaches the method of claim 1 wherein said applying step includes applying an insulative tape to the portion of a plurality of conductors (the Examiner takes the position that since the resin is an adhesive that softens when melted it functions like a tape).
- 9. Pertaining to claim 9, Shimanuki teaches the method of claim 1 wherein said applying step includes applying a continuous bead of the insulative material across only a portion of at least two of a plurality of conductors providing interconnection between elements in the semiconductor device (see **Drawing 13**).



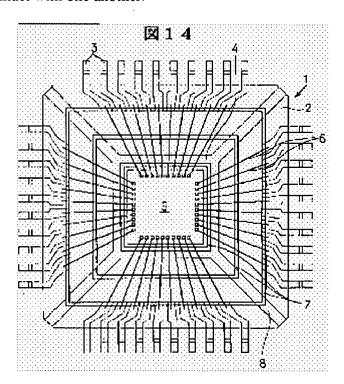
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10. Pertaining tot claim 10, <u>Shimanuki</u> teaches the method of claim 1 wherein said applying step includes applying the insulative material around a peripheral portion of an inner element of the semiconductor device.

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11. Pertaining to claim 11, <u>Shimanuki</u> teaches the method of claim 1 wherein said applying step includes applying the insulative material in at least two distinct structures around a peripheral portion of an inner element of the semiconductor device, the two structures not being in contact with one another.



Objections

12. Claims 4, 5 and 5 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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Conclusion

- 13. Any inquiry concerning this communication or earlier communications from the examiner should be directed to W. David Coleman whose telephone number is 571-272-1856. The examiner can normally be reached on 9:00 AM-5:00 PM.
- 14. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Olik Chaudhuri can be reached on 571-272-1855. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.
- 15. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

W. David Coleman Primary Examiner Art Unit 2823